Report Test 1

General Information

Model Name: TC-500S2U 115V Customer: Serial No: 0603002415
Order No.: Lot No.: Total Load No.: 6
Environment: Inspector: root
MM_DD_YY: 2008/03/13 Begin Time: 上午 11:17:0 End Time: 上午 11:20:3

SEQ.1: Set Up Function ---------- PASS

Vin_Port (1-4) = 1 Vin type = AC
ON Phase (mS) = 0.000 OFF Phase = 0.000
Setup off(Sec) = 1.000 PAUSE function= N
Display Message= PLEASE PRESS ENTER

Relay status: Pre value-1 = 00 Pre value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
Change state delay: For relay = 1.000 For TTL = 0.000
Ext. device GPIB address = * GPIB EOS byte = 1
Message for ext. device = V10

	Load Name	MODE	Ifs	Vfs	Meas	BITS
1.	5V	I	50	10	UUT	0000
2.	12V	I	50	100	UUT	0000
3.	12V	I	50	100	UUT	0000
4.	-12V	I	1	100	UUT	0000
5.	+3.3V	I	50	10	UUT	0000
6.	+5VSB	I	20	10	UUT	0000

SEQ.2: Hold On Adjust (OUTPUT-V-TEST) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Von	BITS
1.	5V	I	50	10	UUT	300	3.500	0000

2.	12V	I	50	100	UUT	300	8.000	0000
3.	12V	I	50	100	UUT	300	8.000	0000
4.	-12V	I	1	100	UUT	300	-8.000	0000
5.	+3.3V	I	50	10	UUT	300	2.000	0000
6.	+5VSB	I	20	10	UUT	300	3.500	0000

Ld	I/R	Slew Rate	Vdc Max	Vdc Min	Vdc Read
1.	2.000	0.010	5.200	5.000	5.070
2.	1.500	0.010	12.400	11.800	12.112
3.	1.000	0.010	12.400	11.800	12.106
4.	0.100	0.010	-13.200	-10.800	-11.912
5.	1.000	0.010	3.465	3.135	3.299
6.	0.100	0.010	5.250	5.000	5.151

SEQ.3: Hold Up & Sequence Test (PF TIME) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 6.000 UUT OFF Time = 3.000

Load	Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	19.000
2.	12V	I	50	100	UUT	8.000	0000	0.010	13.000
3.	12V	I	50	100	UUT	8.000	0000	0.010	13.000
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.800
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	16.500
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	2.000

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb
1.	2	L	7	L	4.750	5.250
2.	2	L	7	L	11.400	12.600
3.	2	L	7	L	11.400	12.600
4.	2	L	7	L	-10.800	-13.200
5.	2	L	7	L	3.135	3.460
6.	2	L	7	L	4.750	5.250

Ld	Thd Max	Thd Min	Thd Read	Tsb Min	Tsb Read

1.	*	1.000	6.655	*	> 1.024
2.	*	1.000	5.319	*	> 1.024
3.	*	1.000	5.308	*	> 1.024
4.	*	1.000	6.348	*	> 1.024
5.	*	1.000	6.954	*	> 1.024
6.	*	*	508.143	*	> 1.024

Ref Ton from LOAD: *

	Max	Min	Reading
Tds	*	*	5.308
Tdl	*	*	508.143
Tdls	*		502.835

Ld	Thds Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.4: Combine Regulation Test (115V) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	115.000	Vin-2 =	115.000	Vin-3 =	115.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	1.000	Meas. Time =	1.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3

1.	0000	0000	0000	0.010	17.500	1.000	6.250
2.	0000	0000	0000	0.010	12.500	1.500	4.000
3.	0000	0000	0000	0.010	12.500	1.000	4.000
4.	0000	0000	0000	0.010	0.800	0.100	0.200
5.	0000	0000	0000	0.010	1.000	1.000	5.500
6.	0000	0000	0000	0.010	2.000	0.100	0.500

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.985	5.085	5.041
2.	12.600	11.400	12.056	12.125	12.087
3.	12.600	11.400	12.018	12.118	12.068
4.	-13.200	-10.800	-11.887	-11.918	-11.931
5.	3.465	3.135	3.288	3.300	3.289
6.	5.250	4.750	4.856	5.160	5.061

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.023	0.004	0.008
2.	0.120	*	0.018	0.011	0.011
3.	0.120	*	0.017	0.013	0.013
4.	0.120	*	0.020	0.012	0.013
5.	0.050	*	0.003	0.003	0.005
6.	0.050	*	0.010	0.001	0.003

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.100	0.056
2.	*	*	0.068	0.031
3.	*	*	0.100	0.050
4.	*	*	0.031	0.043
5.	*	*	0.011	0.001
6.	*	*	0.303	0.205

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.003	0.000	0.000
2.	*	0.006	0.005	0.006
3.	*	0.007	0.006	0.006
4.	*	0.008	0.006	0.006
5.	*	0.000	0.000	0.000
6.	*	0.001	0.000	0.000

SEQ.5: Turn On & Sequence Test (132V PG&Overshoot) ---------- PASS

Vin Port	= 1	Vin Type	= AC
ON Phase delay	= 0.000	OFF Phase delay	= 0.000
Vin	= 132.000	Fin	= 63.0
UUT OFF time	= 3.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	1.000
2.	12V	I	50	100	UUT	8.000	0000	0.010	1.500
3.	12V	I	50	100	UUT	8.000	0000	0.010	1.000
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.100
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	1.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	0.100

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb	Vos Check	Vos Reading
1.	7	H	2	H	4.750	5.500	Y	< 5.500
2.	7	H	2	H	11.400	13.200	Y	< 13.200
3.	7	H	2	H	11.400	13.200	Y	< 13.200
4.	7	H	2	H	-10.800	-13.200	Y	< -13.200
5.	7	H	2	H	3.100	3.630	Y	< 3.630
6.	7	H	2	H	4.750	5.250	N	-----

Ld	Ton Max	Ton Min	Ton Read	Tsb Min	Tsb Read
1.	500.000	100.000	279.932	*	-----
2.	500.000	100.000	248.956	*	-----
3.	500.000	100.000	249.439	*	-----
4.	500.000	100.000	271.702	*	-----
5.	500.000	100.000	289.130	*	-----
6.	*	*	402.935	*	-----

Ref Ton from LOAD: *

	Max	Min	Reading/+	Reading/-
Iinruch	*		31.046	46.906

Tds	*	*	248.956
Tdl	*	*	402.935
Tdls	*		153.979

Ld	Tons Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.6: Turn On & Sequence Test (RISE) ---------- PASS

Vin Port	= 1	Vin Type	= AC
ON Phase delay	= 0.000	OFF Phase delay	= 0.000
Vin	= 115.000	Fin	= 60.0
UUT OFF time	= 3.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	17.500
2.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
3.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.800
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	16.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	2.000

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb	Vos Check	Vos Reading
1.	7	H	8	H	0.500	4.750	N	-----
2.	7	H	8	H	1.200	11.400	N	-----
3.	7	H	8	H	1.200	11.400	N	-----
4.	7	H	8	H	-1.200	-11.400	N	-----
5.	7	H	8	H	0.300	3.135	N	-----
6.	7	H	8	H	0.500	4.750	N	-----

Ld	Ton Max	Ton Min	Ton Read	Tsb Min	Tsb Read
1.	120.000	*	41.701	*	> 1.024
2.	120.000	*	71.788	*	< 0.500
3.	120.000	*	72.316	*	< 0.500
4.	120.000	*	50.144	*	> 1.024
5.	120.000	*	17.078	*	> 1.024
6.	*	*	23.517	*	> 1.024

Ref Ton from LOAD: *

	Max	Min	Reading/+	Reading/-
Iinruch	*		1.546	11.593
Tds	*	*	17.078	
Tdl	*	*	72.316	
Tdls	*		55.238	

Ld	Tons Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.7: Turn On & Sequence Test (PG 100V(FULL LOAD)) ---------- PASS

Vin Port	= 1	Vin Type	= AC
ON Phase delay	= 0.000	OFF Phase delay	= 0.000
Vin	= 100.000	Fin	= 60.0
UUT OFF time	= 3.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	18.000
2.	12V	I	50	100	UUT	8.000	0000	0.010	12.000
3.	12V	I	50	100	UUT	8.000	0000	0.010	12.000
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.800
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	16.500
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	2.000

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb	Vos Check	Vos Reading
1.	7	H	2	H	4.750	5.250	N	-----
2.	7	H	2	H	11.400	13.200	N	-----
3.	7	H	2	H	11.400	13.200	N	-----
4.	7	H	2	H	-10.400	-13.200	N	-----
5.	7	H	2	H	3.150	3.465	N	-----
6.	7	H	2	H	4.750	5.250	N	-----

Ld	Ton Max	Ton Min	Ton Read	Tsb Min	Tsb Read
1.	500.000	100.000	296.512	*	-----
2.	500.000	100.000	266.657	*	-----
3.	500.000	100.000	266.246	*	-----
4.	500.000	100.000	292.693	*	-----
5.	500.000	100.000	320.304	*	-----
6.	*	*	1432.959	*	-----

Ref Ton from LOAD: *

	Max	Min	Reading/+	Reading/-
Iinruch	*		1.390	9.375
Tds	*	*	266.246	
Tdl	*	*	1432.959	
Tdls	*		1166.713	

Ld	Tons Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.8: Line Regulation Test (FULL LOAD) ---------- PASS

Vin Port-1 = 1 Vin Port-2 = 1 Vin Port-3 = 1

Vin-1	=	132.000	Vin-2	=	115.000	Vin-3	= 90.000
Fac	=	63.0	Fac-2	=	50.0	Fac-3	= 47.0
Delay Time	=	1.000	Meas. Time	=	1.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	12.000
2.	0000	0000	0000	0.010	12.000
3.	0000	0000	0000	0.010	12.000
4.	0000	0000	0000	0.010	0.400
5.	0000	0000	0000	0.010	16.500
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	5.005	5.005	5.005
2.	12.600	11.400	12.043	12.050	12.050
3.	12.600	11.400	12.006	12.012	12.012
4.	-13.200	-10.800	-11.968	-11.968	-11.968
5.	3.460	3.140	3.267	3.268	3.268
6.	5.250	4.750	4.830	4.831	4.831

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.022	0.022	0.022
2.	0.120	*	0.017	0.019	0.018
3.	0.120	*	0.016	0.016	0.016
4.	0.120	*	0.020	0.019	0.019
5.	0.050	*	0.016	0.016	0.017
6.	0.050	*	0.012	0.013	0.013

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	0.050	0.050	0.000	0.000

2.	0.120	0.120	0.006	0.006
3.	0.120	0.120	0.006	0.006
4.	*	*	0.000	0.000
5.	0.033	0.033	0.000	0.000
6.	*	*	0.000	0.001

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.003	0.003	0.003
2.	*	0.006	0.006	0.006
3.	*	0.007	0.006	0.006
4.	*	0.008	0.007	0.006
5.	*	0.001	0.001	0.001
6.	*	0.002	0.002	0.002

SEQ.9: Combine Regulation Test (90V~132V) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	47.0	Fac-2 =	50.0	Fac-3 =	63.0
Delay Time =	1.000	Meas. Time =	1.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	12.000	8.750	2.000
2.	0000	0000	0000	0.010	12.000	6.250	1.500
3.	0000	0000	0000	0.010	12.000	6.250	1.000
4.	0000	0000	0000	0.010	0.400	0.400	0.100
5.	0000	0000	0000	0.010	16.000	8.000	1.000
6.	0000	0000	0000	0.010	2.000	1.000	0.100

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	5.006	5.029	5.075
2.	12.600	11.400	12.050	12.068	12.131
3.	12.600	11.400	12.018	12.050	12.125
4.	-13.200	-10.800	-11.968	-11.912	-11.918
5.	3.460	3.140	3.268	3.284	3.300
6.	5.250	4.750	4.833	4.988	5.160

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.021	0.012	0.004
2.	0.120	*	0.018	0.014	0.011
3.	0.120	*	0.016	0.012	0.013
4.	0.120	*	0.020	0.015	0.013
5.	0.050	*	0.015	0.007	0.002
6.	0.050	*	0.012	0.007	0.001

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.023	0.068
2.	*	*	0.018	0.081
3.	*	*	0.031	0.106
4.	*	*	-0.056	-0.050
5.	*	*	0.016	0.031
6.	*	*	0.154	0.326

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.003	0.001	0.000
2.	*	0.006	0.006	0.005
3.	*	0.006	0.006	0.006
4.	*	0.006	0.006	0.006
5.	*	0.001	0.000	0.000
6.	*	0.002	0.001	0.000

SEQ.10: Turn On & Sequence Test (NO LOAD) ---------- PASS

Vin Port	= 1	Vin Type	= AC
ON Phase delay	= 0.000	OFF Phase delay	= 0.000
Vin	= 115.000	Fin	= 60.0

UUT OFF time = 3.000

Load	Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	2.000
2.	12V	I	50	100	UUT	8.000	0000	0.010	1.500
3.	12V	I	50	100	UUT	8.000	0000	0.010	1.000
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.100
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	1.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	0.100

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb	Vos Check	Vos Reading
1.	7	H	2	H	4.750	5.500	N	-----
2.	7	H	2	H	11.400	13.200	N	-----
3.	7	H	2	H	11.400	13.200	N	-----
4.	7	H	2	H	-10.800	-13.200	N	-----
5.	7	H	2	H	3.100	3.630	N	-----
6.	7	H	2	H	4.750	5.250	N	-----

Ld	Ton Max	Ton Min	Ton Read	Tsb Min	Tsb Read
1.	*	*	290.440	*	-----
2.	*	*	260.000	*	-----
3.	*	*	260.535	*	-----
4.	*	*	282.430	*	-----
5.	*	*	299.864	*	-----
6.	*	*	409.823	*	-----

Ref Ton from LOAD: *

	Max	Min	Reading/+	Reading/-
Iinruch	*		50.109	49.812
Tds	*	*	260.000	
Tdl	*	*	409.823	
Tdls	*		149.823	

Ld	Tons Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----

3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.11: Load Regulation Test (+5V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 1.000 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	25.000	12.500	2.000
2.	0000	0000	0000	0.010	12.500	12.500	12.500
3.	0000	0000	0000	0.010	12.500	12.500	12.500
4.	0000	0000	0000	0.010	0.800	0.800	0.800
5.	0000	0000	0000	0.010	4.600	4.600	4.600
6.	0000	0000	0000	0.010	2.000	2.000	2.000

Test On Which Load = 1

	Max	Min	Read-1	Read-2	Read-3
Vdc	5.250	4.750	4.948	5.009	5.068
Vpp	0.050	*	0.040	0.021	0.020
Vn	*		0.007	0.003	0.002

dV(+) = * dV(-) = * dV21 = 0.061 dV31 = 0.120

SEQ.12: Load Regulation Test (+12V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	17.500	17.500	17.500
2.	0000	0000	0000	0.010	16.000	8.000	1.500
3.	0000	0000	0000	0.010	9.000	9.000	9.000
4.	0000	0000	0000	0.010	0.800	0.800	0.800
5.	0000	0000	0000	0.010	16.000	16.000	16.000
6.	0000	0000	0000	0.010	2.000	2.000	2.000

Test On Which Load = 2

	Max	Min	Read-1	Read-2	Read-3
Vdc	12.600	11.400	12.043	12.050	12.081
Vpp	0.120	*	0.018	0.015	0.015
Vn	*		0.005	0.005	0.005

dV(+) = * dV(-) = * dV21 = 0.006 dV31 = 0.037

SEQ.13: Load Regulation Test (+3.3V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000

6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	24.060	24.060	24.060
2.	0000	0000	0000	0.010	13.760	13.760	13.760
3.	0000	0000	0000	0.010	13.760	13.760	13.760
4.	0000	0000	0000	0.010	0.800	0.800	0.800
5.	0000	0000	0000	0.010	9.000	5.000	1.000
6.	0000	0000	0000	0.010	2.000	2.000	2.000

Test On Which Load = 5

	Max	Min	Read-1	Read-2	Read-3
Vdc	3.465	3.135	3.272	3.267	3.285
Vpp	0.050	*	0.010	0.014	0.005
Vn	*		0.001	0.001	0.000

dV(+) = * dV(-) = * dV21 = -0.005 dV31 = 0.013

SEQ.14: Cross Regulation Test (+5V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500 Meas. Time = 1.000

Load	Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	25.000	25.000	25.000
2.	0000	0000	0000	0.010	7.000	3.500	1.500
3.	0000	0000	0000	0.010	7.000	3.500	1.000
4.	0000	0000	0000	0.010	0.100	0.400	0.100
5.	0000	0000	0000	0.010	1.000	1.500	1.000

6.	0000	0000	0000	0.010	0.100	0.100	0.100

Test On Which Load = 1

	Max	Min	Read-1	Read-2	Read-3
Vdc	5.250	4.750	4.956	4.959	4.962
Vpp	0.050	*	0.029	0.019	0.016
Vn	*		0.005	0.003	0.003

dV(+) = * dV(-) = * dV21 = 0.002 dV31 = 0.005

SEQ.15: Cross Regulation Test (+12V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	2.000	4.000	6.000
2.	0000	0000	0000	0.010	16.000	16.000	16.000
3.	0000	0000	0000	0.010	16.000	8.000	1.000
4.	0000	0000	0000	0.010	0.100	0.100	0.100
5.	0000	0000	0000	0.010	24.000	12.000	1.000
6.	0000	0000	0000	0.010	0.100	0.800	0.400

Test On Which Load = 2

	Max	Min	Read-1	Read-2	Read-3
Vdc	12.600	11.400	12.056	12.043	12.025
Vpp	0.120	*	0.025	0.018	0.014
Vn	*		0.006	0.005	0.005

dV(+) = * dV(-) = * dV21 = -0.012 dV31 = -0.031

SEQ.16: Cross Regulation Test (+3.3V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	24.060	6.500	2.000
2.	0000	0000	0000	0.010	13.760	1.500	2.000
3.	0000	0000	0000	0.010	13.760	1.500	2.000
4.	0000	0000	0000	0.010	0.800	0.100	0.100
5.	0000	0000	0000	0.010	9.000	9.000	9.000
6.	0000	0000	0000	0.010	2.000	0.100	0.100

Test On Which Load = 5

	Max	Min	Read-1	Read-2	Read-3
Vdc	3.465	3.135	3.272	3.285	3.285
Vpp	0.050	*	0.010	0.008	0.006
Vn	*		0.001	0.000	0.000

dV(+) = * dV(-) = * dV21 = 0.012 dV31 = 0.013

SEQ.17: Hold Up & Sequence Test (HOLD UP TIME) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.800 UUT OFF Time = 3.000

Load	Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	17.500
2.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
3.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.800
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	16.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	2.000

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb
1.	1	L	7	L	4.750	5.250
2.	1	L	7	L	11.400	12.600
3.	1	L	7	L	11.400	12.600
4.	1	L	7	L	-10.800	-13.200
5.	1	L	7	L	3.100	3.460
6.	1	L	7	L	4.750	5.250

Ld	Thd Max	Thd Min	Thd Read	Tsb Min	Tsb Read
1.	*	16.000	25.818	*	> 1.024
2.	*	16.000	24.451	*	> 1.024
3.	*	16.000	24.439	*	> 1.024
4.	*	16.000	25.440	*	> 1.024
5.	*	16.000	26.157	*	> 1.024
6.	*	*	553.943	*	> 1.024

Ref Ton from LOAD: *

	Max	Min	Reading
Tds	*	*	24.439
Tdl	*	*	553.943
Tdls	*		529.504

Ld	Thds Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----

6.	*	*	*	-----

SEQ.18: OPP Test (115V) ---------- PASS

Vin	= 115.000	Fin	= 60.0	Meas delay	= 1.000
Delay Time	= 1.000	Step Time	= 0.200	UUT OFF Time	= 3.000
Step No.	= 12	Trip Port	= M1	Trig Type	= DC
Vtrig	= 0.500	Trig Slope	= FALL		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Von	BITS
1.	5V	I	50	10	UUT	300	3.500	0000
2.	12V	I	50	100	UUT	300	8.000	0000
3.	12V	I	50	100	UUT	300	8.000	0000
4.	-12V	I	1	100	UUT	300	-8.000	0000
5.	+3.3V	I	50	10	UUT	300	3.500	0000
6.	+5VSB	I	20	10	UUT	300	3.500	0000

	Max	Min	Reading/+	Reading/-
OPP Point	690.000	506.000	674.799	
Topp	*	*	45.495	
Iinpk	*	*	0.326	0.871

Ld	I/R Start	I/R End	I/R rec	Rise	V-disable Max	V-disable Min	V-disable Reading
1.	17.500	45.000	2.000	0.010	*	*	0.000
2.	12.500	20.000	1.000	0.010	*	*	0.006
3.	12.500	20.000	1.000	0.010	*	*	0.006
4.	0.800	0.800	0.100	0.010	*	*	-0.012
5.	16.000	45.000	2.000	0.010	*	*	0.000
6.	2.000	2.000	0.200	0.010	*	*	4.967

Test No.	Test Name	Port	Meas. Type	Spec. Max	Spec. Min	Meas. Read
1.		*	DC	*	*	-----
2.		*	DC	*	*	-----
3.		*	DC	*	*	-----
4.		*	DC	*	*	-----
5.		*	DC	*	*	-----

6.	*	DC	*	*	-----
7.	*	DC	*	*	-----
8.	*	DC	*	*	-----
9.	*	DC	*	*	-----
10.	*	DC	*	*	-----

SEQ.19: Input/Output Test (EFF 65%) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 2.000 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	1M	3.500
2.	12V	I	50	100	UUT	0000	300	1M	8.000
3.	12V	I	50	100	UUT	0000	300	1M	8.000
4.	-12V	I	1	100	UUT	0000	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	0000	300	1M	2.000
6.	+5VSB	I	20	10	UUT	0000	300	1M	3.500

	Max	Min	Reading		Max	Min	Reading
Iinrms	8.00000	*	6.00800	Iinpk+	80.00000	*	9.05800
Pin	*	*	670.2500	Iinpk-			9.04900
Pdc	*	*	459.172	Pf	1.000	0.700	0.982
Eff	*	65.000	68.508	Vin	*	*	113.550

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.010	17.500	*	*	17.487	*	0.003
2.	0.010	12.500	*	*	12.484	*	0.006
3.	0.010	12.500	*	*	12.500	*	0.006
4.	0.010	0.800	*	*	0.800	*	0.007
5.	0.010	16.000	*	*	15.990	*	0.001
6.	0.010	2.000	*	*	2.000	*	0.002

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read
1.	5.250	4.750	4.982	0.050	*	0.024
2.	12.600	11.400	12.056	0.120	*	0.018
3.	12.600	11.400	12.012	0.120	*	0.020
4.	-13.200	-10.800	-11.925	0.120	*	0.019

5.	3.465	3.143	3.266	0.050	*	0.016
6.	5.250	4.750	4.816	0.050	*	0.016

SEQ.20: OLP Test (+5V) ---------- PASS

Vin	=	115.000	Fin	=	60.0	Test on LOAD :	1
Delay Time	=	0.500	Step Time	=	0.100	UUT OFF Time =	3.000
I/R Start	=	17.000	I/R End	=	45.000	I/R Step =	1.000
I/R Recovery	=	1.000	Volp	=	0.100	Vrec =	*

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Von	Rise	I/R
1.	5V	I	50	10	UUT	0000	3.500	0.010	17.000
2.	12V	I	50	100	UUT	0000	8.000	0.010	6.500
3.	12V	I	50	100	UUT	0000	8.000	0.010	6.500
4.	-12V	I	1	100	UUT	0000	-8.000	0.010	0.400
5.	+3.3V	I	50	10	UUT	0000	2.000	0.010	8.000
6.	+5VSB	I	20	10	UUT	0000	3.500	0.010	2.000

	Max	Min	Reading
Trip Point	43.000	28.000	38.000
Trip Time	*	*	23.964
Recovery Time	*	*	-----

SEQ.21: OLP Test (+3.3V) ---------- PASS

Vin	=	115.000	Fin	=	60.0	Test on LOAD :	5
Delay Time	=	0.500	Step Time	=	0.100	UUT OFF Time =	3.000
I/R Start	=	16.000	I/R End	=	45.000	I/R Step =	1.000
I/R Recovery	=	1.000	Volp	=	0.100	Vrec =	*

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Von	Rise	I/R
1.	5V	I	50	10	UUT	0000	3.500	0.010	8.500
2.	12V	I	50	100	UUT	0000	8.000	0.010	6.500
3.	12V	I	50	100	UUT	0000	8.000	0.010	6.500
4.	-12V	I	1	100	UUT	0000	-8.000	0.010	0.400
5.	+3.3V	I	50	10	UUT	0000	2.000	0.010	16.000

6.	+5VSB	I	20	10	UUT	0000	3.500	0.010	2.000

	Max	Min	Reading
Trip Point	40.000	28.000	37.000
Trip Time	*	*	8.930
Recovery Time	*	*	-----

SEQ.22: OLP Test (+12V1) ---------- PASS

Vin	=	115.000	Fin	=	60.0	Test on LOAD :		2	
Delay Time	=	0.500	Step Time	=	0.100	UUT OFF Time	=	3.000	
I/R Start	=	6.000	I/R End	=	35.000	I/R Step	=	1.000	
I/R Recovery	=	0.500	Volp	=	0.100	Vrec	=	*	

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Von	Rise	I/R
1.	5V	I	50	10	UUT	0000	3.500	0.010	8.500
2.	12V	I	50	100	UUT	0000	8.000	0.010	6.000
3.	12V	I	50	100	UUT	0000	8.000	0.010	20.000
4.	-12V	I	1	100	UUT	0000	-8.000	0.010	0.400
5.	+3.3V	I	50	10	UUT	0000	2.000	0.010	8.000
6.	+5VSB	I	20	10	UUT	0000	3.500	0.010	2.000

	Max	Min	Reading
Trip Point	27.000	17.600	24.000
Trip Time	*	*	5.177
Recovery Time	*	*	-----

SEQ.23: OLP Test (+12V2) ---------- PASS

Vin	=	115.000	Fin	=	60.0	Test on LOAD :		3	
Delay Time	=	0.500	Step Time	=	0.100	UUT OFF Time	=	3.000	
I/R Start	=	6.000	I/R End	=	35.000	I/R Step	=	1.000	
I/R Recovery	=	0.500	Volp	=	0.100	Vrec	=	*	

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Von	Rise	I/R
1.	5V	I	50	10	UUT	0000	3.500	0.010	8.500

2.	12V	I	50	100	UUT	0000	8.000	0.010	20.000
3.	12V	I	50	100	UUT	0000	8.000	0.010	6.000
4.	-12V	I	1	100	UUT	0000	-8.000	0.010	0.400
5.	+3.3V	I	50	10	UUT	0000	2.000	0.010	8.000
6.	+5VSB	I	20	10	UUT	0000	3.500	0.010	2.000

	Max	Min	Reading
Trip Point	27.000	17.700	24.000
Trip Time	*	*	5.357
Recovery Time	*	*	-----

SEQ.24: Short Circuit Test (+5V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500
Short Time = 0.500 UUT OFF Time = 3.000 Which Load = 1

Load	Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Slew Rate	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	8.750
2.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
3.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.400
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	8.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	1.000

Ld	Vdisable Max	Vdisable Min	Vdisable Read
1.	*	*	0.000
2.	3.600	*	0.012
3.	3.600	*	0.006
4.	*	*	-0.012
5.	*	*	0.001
6	*	*	5.068

Relay status: Pre value-1 = 00 Pre value-2 = 00
Relay status: Post value-1 = 00 Post value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
TTL status : Post value-1 = 0000 Post value-2 = 0000
Change state delay: For relay = 0.500 For TTL = 0.000

	Maximum	Minimum	Reading/+	Reading/-
Isc-pk	*		74.650	
Isc-rms	*	*	0.005	
Iin-pk	*	*	0.611	0.307
Pin	*	*	7.656	

Test No.	Test Name	Port	Meas. Type	Spec. Max	Spec. Min	Meas. Read
1.		*	DC	*	*	-----
2.		*	DC	*	*	-----
3.		*	DC	*	*	-----
4.		*	DC	*	*	-----
5.		*	DC	*	*	-----
6.		*	DC	*	*	-----
7.		*	DC	*	*	-----
8.		*	DC	*	*	-----
9.		*	DC	*	*	-----
10.		*	DC	*	*	-----

SEQ.25: Short Circuit Test (+3.3V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500
Short Time = 0.500 UUT OFF Time = 3.000 Which Load = 5

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Slew Rate	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	8.750
2.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
3.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.400
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	8.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	1.000

Ld	Vdisable Max	Vdisable Min	Vdisable Read
1.	2.500	*	0.010
2.	*	*	0.012
3.	*	*	0.012
4.	*	*	-0.012

5.	*	*	0.000
6	*	*	5.069

Relay status: Pre value-1 = 00 Pre value-2 = 00
Relay status: Post value-1 = 00 Post value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
TTL status : Post value-1 = 0000 Post value-2 = 0000
Change state delay: For relay = 0.500 For TTL = 0.000

	Maximum	Minimum	Reading/+	Reading/-
Isc-pk	*		45.475	
Isc-rms	*	*	0.004	
Iin-pk	*	*	0.612	0.303
Pin	*	*	8.312	

Test No.	Test Name	Port	Meas. Type	Spec. Max	Spec. Min	Meas. Read
1.		*	DC	*	*	-----
2.		*	DC	*	*	-----
3.		*	DC	*	*	-----
4.		*	DC	*	*	-----
5.		*	DC	*	*	-----
6.		*	DC	*	*	-----
7.		*	DC	*	*	-----
8.		*	DC	*	*	-----
9.		*	DC	*	*	-----
10.		*	DC	*	*	-----

SEQ.26: Short Circuit Test (+12V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500
Short Time = 0.500 UUT OFF Time = 3.000 Which Load = 2

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Slew Rate	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	8.750
2.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
3.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.400

5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	8.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	1.000

Ld	Vdisable Max	Vdisable Min	Vdisable Read
1.	2.500	*	0.010
2.	*	*	0.006
3.	*	*	0.006
4.	*	*	-0.012
5.	*	*	0.001
6	*	*	5.068

Relay status: Pre value-1 = 00 Pre value-2 = 00
Relay status: Post value-1 = 00 Post value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
TTL status : Post value-1 = 0000 Post value-2 = 0000
Change state delay: For relay = 0.500 For TTL = 0.000

	Maximum	Minimum	Reading/+	Reading/-
Isc-pk	*		142.775	
Isc-rms	*	*	0.004	
Iin-pk	*	*	0.319	0.586
Pin	*	*	8.406	

Test No.	Test Name	Port	Meas. Type	Spec. Max	Spec. Min	Meas. Read
1.		*	DC	*	*	-----
2.		*	DC	*	*	-----
3.		*	DC	*	*	-----
4.		*	DC	*	*	-----
5.		*	DC	*	*	-----
6.		*	DC	*	*	-----
7.		*	DC	*	*	-----
8.		*	DC	*	*	-----
9.		*	DC	*	*	-----
10.		*	DC	*	*	-----

SEQ.27: Short Circuit Test (-12V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500
Short Time = 0.500 UUT OFF Time = 3.000 Which Load = 4

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Slew Rate	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	8.750
2.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
3.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.400
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	8.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	1.000

Ld	Vdisable Max	Vdisable Min	Vdisable Read
1.	*	*	0.010
2.	3.600	*	0.012
3.	3.600	*	0.006
4.	*	*	-0.012
5.	*	*	0.001
6	*	*	5.068

Relay status: Pre value-1 = 00 Pre value-2 = 00
Relay status: Post value-1 = 00 Post value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
TTL status : Post value-1 = 0000 Post value-2 = 0000
Change state delay: For relay = 0.500 For TTL = 0.000

	Maximum	Minimum	Reading/+	Reading/-
Isc-pk	*		31.100	
Isc-rms	*	*	0.005	
Iin-pk	*	*	0.257	0.582
Pin	*	*	8.437	

Test No.	Test Name	Port	Meas. Type	Spec. Max	Spec. Min	Meas. Read
1.		*	DC	*	*	-----
2.		*	DC	*	*	-----
3.		*	DC	*	*	-----
4.		*	DC	*	*	-----
5.		*	DC	*	*	-----
6.		*	DC	*	*	-----

7.	*	DC	*	*	-----
8.	*	DC	*	*	-----
9.	*	DC	*	*	-----
10.	*	DC	*	*	-----

SEQ.28: Turn On & Sequence Test (TTURN ON DELAY) ---------- PASS

Vin Port	= 1	Vin Type	= AC
ON Phase delay	= 0.000	OFF Phase delay	= 0.000
Vin	= 115.000	Fin	= 60.0
UUT OFF time	= 3.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	17.500
2.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
3.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.800
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	16.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	2.000

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb	Vos Check	Vos Reading
1.	1	H	7	H	4.750	5.500	Y	< 5.500
2.	1	H	7	H	11.400	13.200	Y	< 13.200
3.	1	H	7	H	11.400	13.200	Y	< 13.200
4.	1	H	7	H	-10.800	-13.200	Y	< -13.200
5.	1	H	7	H	3.100	3.630	Y	< 3.630
6.	1	H	7	H	4.750	5.250	N	-----

Ld	Ton Max	Ton Min	Ton Read	Tsb Min	Tsb Read
1.	2000.000	*	444.326	*	-----
2.	2000.000	*	474.004	*	-----
3.	2000.000	*	474.553	*	-----
4.	2000.000	*	451.168	*	-----
5.	2000.000	*	419.447	*	-----
6.	*	*	198.488	*	> 1.024

Ref Ton from LOAD: *

	Max	Min	Reading/+	Reading/-
Iinruch	79.000		12.343	16.109
Tds	*	*	198.488	
Tdl	*	*	474.553	
Tdls	*		276.065	

Ld	Tons	Source LOAD No.	Td Max	Td Min	Td Read
1.		*	*	*	-----
2.		*	*	*	-----
3.		*	*	*	-----
4.		*	*	*	-----
5.		*	*	*	-----
6.		*	*	*	-----

Report Test 1

General Information

Model Name: TC-500S2U 115V	Customer:		Serial No: 0603002416
Order No.:	Lot No.:		Total Load No.: 6
Environment:	Inspector:	root	
MM_DD_YY: 2008/03/13	Begin Time: 上午 11:26:2		End Time: 上午 11:29:0

SEQ.1: Set Up Function ---------- PASS

Vin_Port (1-4) =	1	Vin type =	AC
ON Phase (mS) =	0.000	OFF Phase =	0.000
Setup off(Sec) =	1.000	PAUSE function=	N

Display Message= PLEASE PRESS ENTER

Relay status: Pre value-1 = 00 Pre value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
Change state delay: For relay = 1.000 For TTL = 0.000
Ext. device GPIB address = * GPIB EOS byte = 1
Message for ext. device = V10

	Load Name	MODE	Ifs	Vfs	Meas	BITS
1.	5V	I	50	10	UUT	0000
2.	12V	I	50	100	UUT	0000
3.	12V	I	50	100	UUT	0000
4.	-12V	I	1	100	UUT	0000
5.	+3.3V	I	50	10	UUT	0000
6.	+5VSB	I	20	10	UUT	0000

SEQ.2: Hold On Adjust (OUTPUT-V-TEST) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Von	BITS
1.	5V	I	50	10	UUT	300	3.500	0000
2.	12V	I	50	100	UUT	300	8.000	0000
3.	12V	I	50	100	UUT	300	8.000	0000
4.	-12V	I	1	100	UUT	300	-8.000	0000
5.	+3.3V	I	50	10	UUT	300	2.000	0000
6.	+5VSB	I	20	10	UUT	300	3.500	0000

Ld	I/R	Slew Rate	Vdc Max	Vdc Min	Vdc Read
1.	2.000	0.010	5.200	5.000	5.067
2.	1.500	0.010	12.400	11.800	12.100
3.	1.000	0.010	12.400	11.800	12.093
4.	0.100	0.010	-13.200	-10.800	-12.000
5.	1.000	0.010	3.465	3.135	3.299
6.	0.100	0.010	5.250	5.000	5.193

SEQ.3: Hold Up & Sequence Test (PF TIME) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 6.000 UUT OFF Time = 3.000

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	19.000
2.	12V	I	50	100	UUT	8.000	0000	0.010	13.000
3.	12V	I	50	100	UUT	8.000	0000	0.010	13.000
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.800
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	16.500
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	2.000

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb
1.	2	L	7	L	4.750	5.250
2.	2	L	7	L	11.400	12.600
3.	2	L	7	L	11.400	12.600
4.	2	L	7	L	-10.800	-13.200
5.	2	L	7	L	3.135	3.460
6.	2	L	7	L	4.750	5.250

Ld	Thd Max	Thd Min	Thd Read	Tsb Min	Tsb Read
1.	*	1.000	9.523	*	> 1.024
2.	*	1.000	7.351	*	> 1.024
3.	*	1.000	7.329	*	> 1.024
4.	*	1.000	8.658	*	> 1.024
5.	*	1.000	9.680	*	> 1.024
6.	*	*	484.946	*	> 1.024

Ref Ton from LOAD: *

	Max	Min	Reading
Tds	*	*	7.329
Tdl	*	*	484.946
Tdls	*		477.617

Ld	Thds Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----

3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.4: Combine Regulation Test (115V) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	115.000	Vin-2 =	115.000	Vin-3 =	115.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	1.000	Meas. Time =	1.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	17.500	1.000	6.250
2.	0000	0000	0000	0.010	12.500	1.500	4.000
3.	0000	0000	0000	0.010	12.500	1.000	4.000
4.	0000	0000	0000	0.010	0.800	0.100	0.200
5.	0000	0000	0000	0.010	1.000	1.000	5.500
6.	0000	0000	0000	0.010	2.000	0.100	0.500

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.986	5.082	5.038
2.	12.600	11.400	12.050	12.112	12.075
3.	12.600	11.400	12.006	12.106	12.056
4.	-13.200	-10.800	-11.975	-12.000	-12.012
5.	3.465	3.135	3.288	3.299	3.288
6.	5.250	4.750	4.890	5.200	5.100

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD

1.	0.050	*	0.022	0.004	0.008
2.	0.120	*	0.019	0.011	0.011
3.	0.120	*	0.016	0.012	0.011
4.	0.120	*	0.018	0.011	0.012
5.	0.050	*	0.004	0.002	0.004
6.	0.050	*	0.012	0.001	0.003

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.095	0.051
2.	*	*	0.062	0.025
3.	*	*	0.100	0.050
4.	*	*	0.025	0.037
5.	*	*	0.011	0.000
6.	*	*	0.310	0.209

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.003	0.000	0.000
2.	*	0.006	0.006	0.006
3.	*	0.007	0.006	0.006
4.	*	0.008	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.002	0.000	0.000

SEQ.5: Turn On & Sequence Test (132V PG&Overshoot) ---------- PASS

Vin Port	= 1	Vin Type	= AC
ON Phase delay	= 0.000	OFF Phase delay	= 0.000
Vin	= 132.000	Fin	= 63.0
UUT OFF time	= 3.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	1.000
2.	12V	I	50	100	UUT	8.000	0000	0.010	1.500
3.	12V	I	50	100	UUT	8.000	0000	0.010	1.000
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.100
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	1.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	0.100

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb	Vos Check	Vos Reading
1.	7	H	2	H	4.750	5.500	Y	< 5.500
2.	7	H	2	H	11.400	13.200	Y	< 13.200
3.	7	H	2	H	11.400	13.200	Y	< 13.200
4.	7	H	2	H	-10.800	-13.200	Y	< -13.200
5.	7	H	2	H	3.100	3.630	Y	< 3.630
6.	7	H	2	H	4.750	5.250	N	-----

Ld	Ton Max	Ton Min	Ton Read	Tsb Min	Tsb Read
1.	500.000	100.000	308.177	*	-----
2.	500.000	100.000	276.488	*	-----
3.	500.000	100.000	276.954	*	-----
4.	500.000	100.000	299.576	*	-----
5.	500.000	100.000	316.280	*	-----
6.	*	*	430.938	*	-----

Ref Ton from LOAD: *

	Max	Min	Reading/+	Reading/-
Iinruch	*		29.875	44.171
Tds	*	*	276.488	
Tdl	*	*	430.938	
Tdls	*		154.450	

Ld	Tons Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.6: Turn On & Sequence Test (RISE) ---------- PASS

Vin Port = 1 Vin Type = AC

ON Phase delay = 0.000 OFF Phase delay = 0.000
Vin = 115.000 Fin = 60.0
UUT OFF time = 3.000

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	17.500
2.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
3.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.800
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	16.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	2.000

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb	Vos Check	Vos Reading
1.	7	H	8	H	0.500	4.750	N	-----
2.	7	H	8	H	1.200	11.400	N	-----
3.	7	H	8	H	1.200	11.400	N	-----
4.	7	H	8	H	-1.200	-11.400	N	-----
5.	7	H	8	H	0.300	3.135	N	-----
6.	7	H	8	H	0.500	4.750	N	-----

Ld	Ton Max	Ton Min	Ton Read	Tsb Min	Tsb Read
1.	120.000	*	42.154	*	> 1.024
2.	120.000	*	73.306	*	< 0.500
3.	120.000	*	73.805	*	< 0.500
4.	120.000	*	51.268	*	> 1.024
5.	120.000	*	19.446	*	> 1.024
6.	*	*	17.304	*	> 1.024

Ref Ton from LOAD: *

	Max	Min	Reading/+	Reading/-
Iinruch	*		1.515	12.640
Tds	*	*	17.304	
Tdl	*	*	73.805	
Tdls	*		56.501	

Ld	Tons Source LOAD No.	Td Max	Td Min	Td Read

1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.7: Turn On & Sequence Test (PG 100V(FULL LOAD)) ---------- PASS

Vin Port	= 1	Vin Type	= AC
ON Phase delay	= 0.000	OFF Phase delay	= 0.000
Vin	= 100.000	Fin	= 60.0
UUT OFF time	= 3.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	18.000
2.	12V	I	50	100	UUT	8.000	0000	0.010	12.000
3.	12V	I	50	100	UUT	8.000	0000	0.010	12.000
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.800
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	16.500
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	2.000

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb	Vos Check	Vos Reading
1.	7	H	2	H	4.750	5.250	N	-----
2.	7	H	2	H	11.400	13.200	N	-----
3.	7	H	2	H	11.400	13.200	N	-----
4.	7	H	2	H	-10.400	-13.200	N	-----
5.	7	H	2	H	3.150	3.465	N	-----
6.	7	H	2	H	4.750	5.250	N	-----

Ld	Ton Max	Ton Min	Ton Read	Tsb Min	Tsb Read
1.	500.000	100.000	306.076	*	-----
2.	500.000	100.000	275.321	*	-----
3.	500.000	100.000	274.909	*	-----
4.	500.000	100.000	301.665	*	-----

5.	500.000	100.000	326.837	*	-----
6.	*	*	548.882	*	-----

Ref Ton from LOAD: *

	Max	Min	Reading/+	Reading/-
Iinruch	*		1.265	8.718
Tds	*	*	274.909	
Tdl	*	*	548.882	
Tdls	*		273.973	

Ld	Tons Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.8: Line Regulation Test (FULL LOAD) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	132.000	Vin-2 =	115.000	Vin-3 =	90.000
Fac =	63.0	Fac-2 =	50.0	Fac-3 =	47.0
Delay Time =	1.000	Meas. Time =	1.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	12.000
2.	0000	0000	0000	0.010	12.000
3.	0000	0000	0000	0.010	12.000

4.	0000	0000	0000	0.010	0.400
5.	0000	0000	0000	0.010	16.500
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	5.000	5.000	5.001
2.	12.600	11.400	12.037	12.043	12.043
3.	12.600	11.400	12.000	12.006	12.006
4.	-13.200	-10.800	-12.050	-12.056	-12.056
5.	3.460	3.140	3.266	3.265	3.265
6.	5.250	4.750	4.864	4.865	4.865

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.022	0.022	0.023
2.	0.120	*	0.018	0.017	0.018
3.	0.120	*	0.016	0.018	0.018
4.	0.120	*	0.018	0.018	0.018
5.	0.050	*	0.010	0.010	0.011
6.	0.050	*	0.012	0.015	0.011

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	0.050	0.050	0.000	0.000
2.	0.120	0.120	0.006	0.006
3.	0.120	0.120	0.006	0.006
4.	*	*	0.006	0.006
5.	0.033	0.033	0.000	0.000
6.	*	*	0.000	0.001

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.003	0.003	0.003
2.	*	0.006	0.006	0.006
3.	*	0.007	0.006	0.006
4.	*	0.008	0.007	0.006
5.	*	0.001	0.001	0.001
6.	*	0.001	0.001	0.001

SEQ.9: Combine Regulation Test (90V~132V) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	47.0	Fac-2 =	50.0	Fac-3 =	63.0
Delay Time =	1.000	Meas. Time =	1.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	12.000	8.750	2.000
2.	0000	0000	0000	0.010	12.000	6.250	1.500
3.	0000	0000	0000	0.010	12.000	6.250	1.000
4.	0000	0000	0000	0.010	0.400	0.400	0.100
5.	0000	0000	0000	0.010	16.000	8.000	1.000
6.	0000	0000	0000	0.010	2.000	1.000	0.100

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	5.001	5.025	5.071
2.	12.600	11.400	12.043	12.050	12.112
3.	12.600	11.400	12.006	12.031	12.112
4.	-13.200	-10.800	-12.050	-12.000	-12.000
5.	3.460	3.140	3.266	3.283	3.298
6.	5.250	4.750	4.867	5.023	5.198

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.023	0.012	0.004
2.	0.120	*	0.020	0.012	0.010
3.	0.120	*	0.021	0.013	0.013
4.	0.120	*	0.018	0.014	0.012
5.	0.050	*	0.011	0.006	0.003
6.	0.050	*	0.012	0.005	0.001

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.023	0.070
2.	*	*	0.006	0.068
3.	*	*	0.025	0.106
4.	*	*	-0.050	-0.050
5.	*	*	0.017	0.032
6.	*	*	0.155	0.331

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.003	0.001	0.000
2.	*	0.006	0.006	0.005
3.	*	0.006	0.006	0.006
4.	*	0.006	0.006	0.006
5.	*	0.001	0.000	0.000
6.	*	0.001	0.001	0.000

SEQ.10: Turn On & Sequence Test (NO LOAD) ---------- PASS

Vin Port	= 1	Vin Type	= AC
ON Phase delay	= 0.000	OFF Phase delay	= 0.000
Vin	= 115.000	Fin	= 60.0
UUT OFF time	= 3.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	2.000
2.	12V	I	50	100	UUT	8.000	0000	0.010	1.500
3.	12V	I	50	100	UUT	8.000	0000	0.010	1.000
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.100
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	1.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	0.100

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb	Vos Check	Vos Reading
1.	7	H	2	H	4.750	5.500	N	-----
2.	7	H	2	H	11.400	13.200	N	-----
3.	7	H	2	H	11.400	13.200	N	-----

4.	7	H	2	H	-10.800	-13.200	N	-----
5.	7	H	2	H	3.100	3.630	N	-----
6.	7	H	2	H	4.750	5.250	N	-----

Ld	Ton Max	Ton Min	Ton Read	Tsb Min	Tsb Read
1.	*	*	307.007	*	-----
2.	*	*	275.797	*	-----
3.	*	*	276.281	*	-----
4.	*	*	298.774	*	-----
5.	*	*	315.449	*	-----
6.	*	*	428.141	*	-----

Ref Ton from LOAD: *

	Max	Min	Reading/+	Reading/-
Iinruch	*		47.546	43.812
Tds	*	*	275.797	
Tdl	*	*	428.141	
Tdls	*		152.344	

Ld	Tons Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.11: Load Regulation Test (+5V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 1.000 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000

6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	25.000	12.500	2.000
2.	0000	0000	0000	0.010	12.500	12.500	12.500
3.	0000	0000	0000	0.010	12.500	12.500	12.500
4.	0000	0000	0000	0.010	0.800	0.800	0.800
5.	0000	0000	0000	0.010	4.600	4.600	4.600
6.	0000	0000	0000	0.010	2.000	2.000	2.000

Test On Which Load = 1

	Max	Min	Read-1	Read-2	Read-3
Vdc	5.250	4.750	4.964	5.004	5.063
Vpp	0.050	*	0.031	0.022	0.021
Vn	*		0.005	0.003	0.002

dV(+) = * dV(-) = * dV21 = 0.040 dV31 = 0.099

SEQ.12: Load Regulation Test (+12V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	17.500	17.500	17.500
2.	0000	0000	0000	0.010	16.000	8.000	1.500
3.	0000	0000	0000	0.010	9.000	9.000	9.000
4.	0000	0000	0000	0.010	0.800	0.800	0.800
5.	0000	0000	0000	0.010	16.000	16.000	16.000

6.	0000	0000	0000	0.010	2.000	2.000	2.000

Test On Which Load = 2

	Max	Min	Read-1	Read-2	Read-3
Vdc	12.600	11.400	12.031	12.031	12.075
Vpp	0.120	*	0.018	0.015	0.016
Vn	*		0.006	0.005	0.006

dV(+) = * dV(-) = * dV21 = 0.000 dV31 = 0.043

SEQ.13: Load Regulation Test (+3.3V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	24.060	24.060	24.060
2.	0000	0000	0000	0.010	13.760	13.760	13.760
3.	0000	0000	0000	0.010	13.760	13.760	13.760
4.	0000	0000	0000	0.010	0.800	0.800	0.800
5.	0000	0000	0000	0.010	9.000	5.000	1.000
6.	0000	0000	0000	0.010	2.000	2.000	2.000

Test On Which Load = 5

	Max	Min	Read-1	Read-2	Read-3
Vdc	3.465	3.135	3.271	3.266	3.285
Vpp	0.050	*	0.009	0.011	0.005
Vn	*		0.001	0.001	0.000

dV(+) = * dV(-) = * dV21 = -0.005 dV31 = 0.013

SEQ.14: Cross Regulation Test (+5V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	25.000	25.000	25.000
2.	0000	0000	0000	0.010	7.000	3.500	1.500
3.	0000	0000	0000	0.010	7.000	3.500	1.000
4.	0000	0000	0000	0.010	0.100	0.400	0.100
5.	0000	0000	0000	0.010	1.000	1.500	1.000
6.	0000	0000	0000	0.010	0.100	0.100	0.100

Test On Which Load = 1

	Max	Min	Read-1	Read-2	Read-3
Vdc	5.250	4.750	4.977	4.977	4.980
Vpp	0.050	*	0.020	0.016	0.015
Vn	*		0.003	0.002	0.003

dV(+) = * dV(-) = * dV21 = 0.000 dV31 = 0.003

SEQ.15: Cross Regulation Test (+12V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	2.000	4.000	6.000
2.	0000	0000	0000	0.010	16.000	16.000	16.000
3.	0000	0000	0000	0.010	16.000	8.000	1.000
4.	0000	0000	0000	0.010	0.100	0.100	0.100
5.	0000	0000	0000	0.010	24.000	12.000	1.000
6.	0000	0000	0000	0.010	0.100	0.800	0.400

Test On Which Load = 2

	Max	Min	Read-1	Read-2	Read-3
Vdc	12.600	11.400	12.050	12.037	12.012
Vpp	0.120	*	0.023	0.018	0.013
Vn	*		0.006	0.006	0.005

dV(+) = * dV(-) = * dV21 = -0.012 dV31 = -0.037

SEQ.16: Cross Regulation Test (+3.3V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	300	1M	3.500
2.	12V	I	50	100	UUT	300	1M	8.000
3.	12V	I	50	100	UUT	300	1M	8.000
4.	-12V	I	1	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	+5VSB	I	20	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R-1	I/R-2	I/R-3
1.	0000	0000	0000	0.010	24.060	6.500	2.000
2.	0000	0000	0000	0.010	13.760	1.500	2.000
3.	0000	0000	0000	0.010	13.760	1.500	2.000
4.	0000	0000	0000	0.010	0.800	0.100	0.100
5.	0000	0000	0000	0.010	9.000	9.000	9.000
6.	0000	0000	0000	0.010	2.000	0.100	0.100

Test On Which Load = 5

	Max	Min	Read-1	Read-2	Read-3
Vdc	3.465	3.135	3.271	3.282	3.283
Vpp	0.050	*	0.008	0.007	0.006
Vn	*		0.001	0.000	0.000

dV(+) = * dV(-) = * dV21 = 0.011 dV31 = 0.011

SEQ.17: Hold Up & Sequence Test (HOLD UP TIME) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.800 UUT OFF Time = 3.000

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	17.500
2.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
3.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.800
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	16.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	2.000

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb
1.	1	L	7	L	4.750	5.250
2.	1	L	7	L	11.400	12.600
3.	1	L	7	L	11.400	12.600
4.	1	L	7	L	-10.800	-13.200
5.	1	L	7	L	3.100	3.460

6.	1	L	7	L	4.750	5.250

Ld	Thd Max	Thd Min	Thd Read	Tsb Min	Tsb Read
1.	*	16.000	29.601	*	> 1.024
2.	*	16.000	27.593	*	> 1.024
3.	*	16.000	27.579	*	> 1.024
4.	*	16.000	28.725	*	> 1.024
5.	*	16.000	29.797	*	> 1.024
6.	*	*	508.915	*	> 1.024

Ref Ton from LOAD: *

	Max	Min	Reading
Tds	*	*	27.579
Tdl	*	*	508.915
Tdls	*		481.336

Ld	Thds Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

SEQ.18: OPP Test (115V) ---------- PASS

Vin	=	115.000	Fin	=	60.0	Meas delay	= 1.000
Delay Time	=	1.000	Step Time	=	0.200	UUT OFF Time	= 3.000
Step No.	=	12	Trip Port	=	M1	Trig Type	= DC
Vtrig	=	0.500	Trig Slope	=	FALL		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Von	BITS
1.	5V	I	50	10	UUT	300	3.500	0000
2.	12V	I	50	100	UUT	300	8.000	0000
3.	12V	I	50	100	UUT	300	8.000	0000
4.	-12V	I	1	100	UUT	300	-8.000	0000
5.	+3.3V	I	50	10	UUT	300	3.500	0000

6.	+5VSB	I	20	10	UUT	300	3.500	0000

	Max	Min	Reading/+	Reading/-
OPP Point	690.000	506.000	641.761	
Topp	*	*	48.810	
Iinpk	*	*	0.328	0.864

Ld	I/R Start	I/R End	I/R rec	Rise	V-disable Max	V-disable Min	V-disable Reading
1.	17.500	45.000	2.000	0.010	*	*	0.000
2.	12.500	20.000	1.000	0.010	*	*	0.006
3.	12.500	20.000	1.000	0.010	*	*	0.000
4.	0.800	0.800	0.100	0.010	*	*	-0.012
5.	16.000	45.000	2.000	0.010	*	*	0.000
6.	2.000	2.000	0.200	0.010	*	*	5.000

Test No.	Test Name	Port	Meas. Type	Spec. Max	Spec. Min	Meas. Read
1.		*	DC	*	*	-----
2.		*	DC	*	*	-----
3.		*	DC	*	*	-----
4.		*	DC	*	*	-----
5.		*	DC	*	*	-----
6.		*	DC	*	*	-----
7.		*	DC	*	*	-----
8.		*	DC	*	*	-----
9.		*	DC	*	*	-----
10.		*	DC	*	*	-----

SEQ.19: Input/Output Test (EFF 65%) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 2.000 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	1M	3.500
2.	12V	I	50	100	UUT	0000	300	1M	8.000
3.	12V	I	50	100	UUT	0000	300	1M	8.000
4.	-12V	I	1	100	UUT	0000	300	1M	-8.000

5.	+3.3V	I	50	10	UUT	0000	300	1M	2.000
6.	+5VSB	I	20	10	UUT	0000	300	1M	3.500

	Max	Min	Reading		Max	Min	Reading
Iinrms	8.00000	*	6.00000	Iinpk+	80.00000	*	8.93600
Pin	*	*	668.6250	Iinpk-			8.97500
Pdc	*	*	459.164	Pf	1.000	0.700	0.981
Eff	*	65.000	68.673	Vin	*	*	113.550

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.010	17.500	*	*	17.484	*	0.003
2.	0.010	12.500	*	*	12.481	*	0.006
3.	0.010	12.500	*	*	12.500	*	0.006
4.	0.010	0.800	*	*	0.800	*	0.007
5.	0.010	16.000	*	*	15.990	*	0.001
6.	0.010	2.000	*	*	2.000	*	0.001

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read
1.	5.250	4.750	4.983	0.050	*	0.023
2.	12.600	11.400	12.050	0.120	*	0.018
3.	12.600	11.400	12.012	0.120	*	0.018
4.	-13.200	-10.800	-12.012	0.120	*	0.018
5.	3.465	3.143	3.264	0.050	*	0.012
6.	5.250	4.750	4.848	0.050	*	0.013

SEQ.20: OLP Test (+5V) ---------- PASS

Vin	= 115.000	Fin	= 60.0	Test on LOAD :	1
Delay Time	= 0.500	Step Time	= 0.100	UUT OFF Time	= 3.000
I/R Start	= 17.000	I/R End	= 45.000	I/R Step	= 1.000
I/R Recovery	= 1.000	Volp	= 0.100	Vrec	= *

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Von	Rise	I/R
1.	5V	I	50	10	UUT	0000	3.500	0.010	17.000
2.	12V	I	50	100	UUT	0000	8.000	0.010	6.500
3.	12V	I	50	100	UUT	0000	8.000	0.010	6.500
4.	-12V	I	1	100	UUT	0000	-8.000	0.010	0.400

5.	+3.3V	I	50	10	UUT	0000	2.000	0.010	8.000
6.	+5VSB	I	20	10	UUT	0000	3.500	0.010	2.000

	Max	Min	Reading
Trip Point	43.000	28.000	34.000
Trip Time	*	*	6.951
Recovery Time	*	*	-----

SEQ.21: OLP Test (+3.3V) ---------- PASS

Vin	=	115.000	Fin	=	60.0	Test on LOAD :		5
Delay Time	=	0.500	Step Time	=	0.100	UUT OFF Time	=	3.000
I/R Start	=	16.000	I/R End	=	45.000	I/R Step	=	1.000
I/R Recovery	=	1.000	Volp	=	0.100	Vrec	=	*

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Von	Rise	I/R
1.	5V	I	50	10	UUT	0000	3.500	0.010	8.500
2.	12V	I	50	100	UUT	0000	8.000	0.010	6.500
3.	12V	I	50	100	UUT	0000	8.000	0.010	6.500
4.	-12V	I	1	100	UUT	0000	-8.000	0.010	0.400
5.	+3.3V	I	50	10	UUT	0000	2.000	0.010	16.000
6.	+5VSB	I	20	10	UUT	0000	3.500	0.010	2.000

	Max	Min	Reading
Trip Point	40.000	28.000	37.000
Trip Time	*	*	6.428
Recovery Time	*	*	-----

SEQ.22: OLP Test (+12V1) ---------- PASS

Vin	=	115.000	Fin	=	60.0	Test on LOAD :		2
Delay Time	=	0.500	Step Time	=	0.100	UUT OFF Time	=	3.000
I/R Start	=	6.000	I/R End	=	35.000	I/R Step	=	1.000
I/R Recovery	=	0.500	Volp	=	0.100	Vrec	=	*

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Von	Rise	I/R

1.	5V	I	50	10	UUT	0000	3.500	0.010	8.500
2.	12V	I	50	100	UUT	0000	8.000	0.010	6.000
3.	12V	I	50	100	UUT	0000	8.000	0.010	20.000
4.	-12V	I	1	100	UUT	0000	-8.000	0.010	0.400
5.	+3.3V	I	50	10	UUT	0000	2.000	0.010	8.000
6.	+5VSB	I	20	10	UUT	0000	3.500	0.010	2.000

	Max	Min	Reading
Trip Point	27.000	17.600	23.000
Trip Time	*	*	5.573
Recovery Time	*	*	-----

SEQ.23: OLP Test (+12V2) ---------- PASS

Vin	= 115.000	Fin	= 60.0	Test on LOAD :		3	
Delay Time	= 0.500	Step Time	= 0.100	UUT OFF Time	=	3.000	
I/R Start	= 6.000	I/R End	= 35.000	I/R Step	=	1.000	
I/R Recovery	= 0.500	Volp	= 0.100	Vrec	=	*	

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Von	Rise	I/R
1.	5V	I	50	10	UUT	0000	3.500	0.010	8.500
2.	12V	I	50	100	UUT	0000	8.000	0.010	20.000
3.	12V	I	50	100	UUT	0000	8.000	0.010	6.000
4.	-12V	I	1	100	UUT	0000	-8.000	0.010	0.400
5.	+3.3V	I	50	10	UUT	0000	2.000	0.010	8.000
6.	+5VSB	I	20	10	UUT	0000	3.500	0.010	2.000

	Max	Min	Reading
Trip Point	27.000	17.700	22.000
Trip Time	*	*	90.840
Recovery Time	*	*	-----

SEQ.24: Short Circuit Test (+5V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500
Short Time = 0.500 UUT OFF Time = 3.000 Which Load = 1

Load	Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Slew Rate	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	8.750
2.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
3.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.400
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	8.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	1.000

Ld	Vdisable Max	Vdisable Min	Vdisable Read
1.	*	*	0.000
2.	3.600	*	0.012
3.	3.600	*	0.006
4.	*	*	-0.012
5.	*	*	0.000
6	*	*	5.102

Relay status: Pre value-1 = 00 Pre value-2 = 00
Relay status: Post value-1 = 00 Post value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
TTL status : Post value-1 = 0000 Post value-2 = 0000
Change state delay: For relay = 0.500 For TTL = 0.000

	Maximum	Minimum	Reading/+	Reading/-
Isc-pk	*		65.350	
Isc-rms	*	*	0.004	
Iin-pk	*	*	0.267	0.539
Pin	*	*	8.531	

Test No.	Test Name	Port	Meas. Type	Spec. Max	Spec. Min	Meas. Read
1.		*	DC	*	*	-----
2.		*	DC	*	*	-----
3.		*	DC	*	*	-----
4.		*	DC	*	*	-----
5.		*	DC	*	*	-----
6.		*	DC	*	*	-----
7.		*	DC	*	*	-----
8.		*	DC	*	*	-----

9.	*	DC	*	*	-----
10.	*	DC	*	*	-----

SEQ.25: Short Circuit Test (+3.3V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500
Short Time = 0.500 UUT OFF Time = 3.000 Which Load = 5

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Slew Rate	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	8.750
2.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
3.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.400
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	8.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	1.000

Ld	Vdisable Max	Vdisable Min	Vdisable Read
1.	2.500	*	0.008
2.	*	*	0.012
3.	*	*	0.006
4.	*	*	-0.012
5.	*	*	0.000
6	*	*	5.103

Relay status: Pre value-1 = 00 Pre value-2 = 00
Relay status: Post value-1 = 00 Post value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
TTL status : Post value-1 = 0000 Post value-2 = 0000
Change state delay: For relay = 0.500 For TTL = 0.000

	Maximum	Minimum	Reading/+	Reading/-
Isc-pk	*		45.450	
Isc-rms	*	*	0.004	
Iin-pk	*	*	0.301	0.532
Pin	*	*	8.468	

Test No.	Test Name	Port	Meas. Type	Spec. Max	Spec. Min	Meas. Read

1.	*	DC	*	*	-----
2.	*	DC	*	*	-----
3.	*	DC	*	*	-----
4.	*	DC	*	*	-----
5.	*	DC	*	*	-----
6.	*	DC	*	*	-----
7.	*	DC	*	*	-----
8.	*	DC	*	*	-----
9.	*	DC	*	*	-----
10.	*	DC	*	*	-----

SEQ.26: Short Circuit Test (+12V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500
Short Time = 0.500 UUT OFF Time = 3.000 Which Load = 2

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Slew Rate	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	8.750
2.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
3.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.400
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	8.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	1.000

Ld	Vdisable Max	Vdisable Min	Vdisable Read
1.	2.500	*	0.008
2.	*	*	0.006
3.	*	*	0.006
4.	*	*	-0.012
5.	*	*	0.001
6	*	*	5.102

Relay status: Pre value-1 = 00 Pre value-2 = 00
Relay status: Post value-1 = 00 Post value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
TTL status : Post value-1 = 0000 Post value-2 = 0000
Change state delay: For relay = 0.500 For TTL = 0.000

	Maximum	Minimum	Reading/+	Reading/-
Isc-pk	*		141.650	
Isc-rms	*	*	0.004	
Iin-pk	*	*	0.543	0.492
Pin	*	*	8.531	

Test No.	Test Name	Port	Meas. Type	Spec. Max	Spec. Min	Meas. Read
1.		*	DC	*	*	-----
2.		*	DC	*	*	-----
3.		*	DC	*	*	-----
4.		*	DC	*	*	-----
5.		*	DC	*	*	-----
6.		*	DC	*	*	-----
7.		*	DC	*	*	-----
8.		*	DC	*	*	-----
9.		*	DC	*	*	-----
10.		*	DC	*	*	-----

SEQ.27: Short Circuit Test (-12V) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 0.500
Short Time = 0.500 UUT OFF Time = 3.000 Which Load = 4

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Slew Rate	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	8.750
2.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
3.	12V	I	50	100	UUT	8.000	0000	0.010	6.250
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.400
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	8.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	1.000

Ld	Vdisable Max	Vdisable Min	Vdisable Read
1.	*	*	0.008
2.	3.600	*	0.012
3.	3.600	*	0.012
4.	*	*	-0.012

5.	*	*	0.001
6	*	*	5.101

Relay status: Pre value-1 = 00 Pre value-2 = 00
Relay status: Post value-1 = 00 Post value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
TTL status : Post value-1 = 0000 Post value-2 = 0000
Change state delay: For relay = 0.500 For TTL = 0.000

	Maximum	Minimum	Reading/+	Reading/-
Isc-pk	*		33.125	
Isc-rms	*	*	0.004	
Iin-pk	*	*	0.305	0.526
Pin	*	*	8.562	

Test No.	Test Name	Port	Meas. Type	Spec. Max	Spec. Min	Meas. Read
1.		*	DC	*	*	-----
2.		*	DC	*	*	-----
3.		*	DC	*	*	-----
4.		*	DC	*	*	-----
5.		*	DC	*	*	-----
6.		*	DC	*	*	-----
7.		*	DC	*	*	-----
8.		*	DC	*	*	-----
9.		*	DC	*	*	-----
10.		*	DC	*	*	-----

SEQ.28: Turn On & Sequence Test (TTURN ON DELAY) ---------- PASS

Vin Port = 1 Vin Type = AC
ON Phase delay = 0.000 OFF Phase delay = 0.000
Vin = 115.000 Fin = 60.0
UUT OFF time = 3.000

	Load Name	MODE	Ifs	Vfs	Meas.	Von	BITS	Rise	I/R
1.	5V	I	50	10	UUT	3.500	0000	0.010	17.500
2.	12V	I	50	100	UUT	8.000	0000	0.010	12.500

3.	12V	I	50	100	UUT	8.000	0000	0.010	12.500
4.	-12V	I	1	100	UUT	-8.000	0000	0.010	0.800
5.	+3.3V	I	50	10	UUT	2.000	0000	0.010	16.000
6.	+5VSB	I	20	10	UUT	3.500	0000	0.010	2.000

Ld	START TRIGG NO.	START TRIGG LEVEL	END TRIGG NO.	END TRIGG LEVEL	Va	Vb	Vos Check	Vos Reading
1.	1	H	7	H	4.750	5.500	Y	< 5.500
2.	1	H	7	H	11.400	13.200	Y	< 13.200
3.	1	H	7	H	11.400	13.200	Y	< 13.200
4.	1	H	7	H	-10.800	-13.200	Y	< -13.200
5.	1	H	7	H	3.100	3.630	Y	< 3.630
6.	1	H	7	H	4.750	5.250	N	-----

Ld	Ton Max	Ton Min	Ton Read	Tsb Min	Tsb Read
1.	2000.000	*	487.806	*	-----
2.	2000.000	*	518.523	*	-----
3.	2000.000	*	519.009	*	-----
4.	2000.000	*	494.907	*	-----
5.	2000.000	*	465.298	*	-----
6.	*	*	241.244	*	> 1.024

Ref Ton from LOAD: *

	Max	Min	Reading/+	Reading/-
Iinruch	79.000		5.828	16.937
Tds	*	*	241.244	
Tdl	*	*	519.009	
Tdls	*		277.765	

Ld	Tons Source LOAD No.	Td Max	Td Min	Td Read
1.	*	*	*	-----
2.	*	*	*	-----
3.	*	*	*	-----
4.	*	*	*	-----
5.	*	*	*	-----
6.	*	*	*	-----

Report Test 1

General Information

Model Name: TC-500S2U Customer: Serial No: 0000000001
Order No.: Lot No.: Total Load No.: 6
Environment: Inspector: root
MM_DD_YY: 2008/03/13 Begin Time: 下午 02:33:0 End Time: 下午 02:38:2

SEQ.1: Set Up Function ---------- PASS

Vin_Port (1-4) = 1 Vin type = AC
ON Phase (mS) = 0.000 OFF Phase = 0.000
Setup off(Sec) = 0.100 PAUSE function= N
Display Message= PLEASE PRESS ENTER

Relay status: Pre value-1 = 00 Pre value-2 = 00
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
Change state delay: For relay = 0 For TTL = 0
Ext. device GPIB address = * GPIB EOS byte = 1
Message for ext. device = V10

	Load Name	MODE	Ifs	Vfs	Meas	BITS
1.	+5V	I	50	10	UUT	0000
2.	+12V1	I	50	100	UUT	0000
3.	+12V2	I	1	10	UUT	0000
4.	-12V	I	1	100	UUT	0000
5.	+3.3V	I	20	100	UUT	0000
6.	SB+5V	I	20	100	UUT	0000

SEQ.2: Hold On Adjust (1. DAN) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Von	BITS
1.	+5V	I	50	10	UUT	300	3.500	0000

2.	+12V1	I	50	100	UUT	300	8.000	0000
3.	+12V2	I	50	100	UUT	300	8.000	0000
4.	-12V	I	50	100	UUT	300	-8.000	0000
5.	+3.3V	I	50	10	UUT	300	2.000	0000
6.	SB+5V	I	50	10	UUT	300	3.500	0000

Ld	I/R	Slew Rate	Vdc Max	Vdc Min	Vdc Read
1.	1.000	0.010	5.250	4.750	5.093
2.	1.000	0.010	12.600	11.400	12.118
3.	1.000	0.010	12.600	11.400	12.112
4.	0.100	0.010	-13.200	-10.920	-11.906
5.	1.000	0.010	3.465	3.135	3.300
6.	0.100	0.010	5.250	4.750	5.220

SEQ.3: Line Regulation Test (1.+5V MAX 90V~132V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	20	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	25.000
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	7.570
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.933	4.934	4.935
2.	12.600	11.400	12.056	12.056	12.056
3.	12.600	11.400	12.006	12.012	12.018
4.	-13.200	-10.920	-11.925	-11.925	-11.931
5.	3.465	3.135	3.266	3.266	3.266
6.	5.250	4.750	4.810	4.811	4.812

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.032	0.030	0.029
2.	0.120	*	0.021	0.021	0.021
3.	0.120	*	0.020	0.020	0.021
4.	0.120	*	0.018	0.019	0.018
5.	0.050	*	0.009	0.009	0.009
6.	0.050	*	0.011	0.010	0.011

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.001	0.002
2.	*	*	0.000	0.000
3.	*	*	0.006	0.012
4.	*	*	0.000	0.006
5.	*	*	0.000	0.000
6.	*	*	0.001	0.001

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.005	0.004	0.004
2.	*	0.007	0.006	0.006
3.	*	0.007	0.007	0.007
4.	*	0.008	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.4: Line Regulation Test (1.+5V MAX 180V~264V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0

Delay Time = 2.000 Meas. Time = 2.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	25.000
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	7.570
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.936	4.936	4.936
2.	12.600	11.400	12.062	12.062	12.062
3.	12.600	11.400	12.018	12.018	12.018
4.	-13.200	-10.920	-11.931	-11.931	-11.931
5.	3.465	3.135	3.266	3.265	3.266
6.	5.250	4.750	4.812	4.813	4.813

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.028	0.028	0.029
2.	0.120	*	0.022	0.021	0.023
3.	0.120	*	0.018	0.019	0.021
4.	0.120	*	0.018	0.019	0.018
5.	0.050	*	0.008	0.008	0.009
6.	0.050	*	0.011	0.011	0.009

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000

4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006
3.	*	0.007	0.006	0.006
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.5: Line Regulation Test (1.+3.3V MAX 90V~132V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	3.000	Meas. Time =	3.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	15.480
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	22.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.981	4.982	4.982

2.	12.600	11.400	12.062	12.062	12.062
3.	12.600	11.400	12.012	12.018	12.018
4.	-13.200	-10.920	-11.950	-11.950	-11.950
5.	3.465	3.135	3.241	3.241	3.241
6.	5.250	4.750	4.797	4.797	4.797

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.027	0.027	0.027
2.	0.120	*	0.022	0.021	0.022
3.	0.120	*	0.020	0.021	0.020
4.	0.120	*	0.020	0.019	0.019
5.	0.050	*	0.033	0.032	0.032
6.	0.050	*	0.012	0.012	0.011

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.000	0.000
3.	*	*	0.006	0.006
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006
3.	*	0.007	0.006	0.007
4.	*	0.007	0.007	0.007
5.	*	0.006	0.006	0.005
6.	*	0.001	0.001	0.001

SEQ.6: Line Regulation Test (1.+3.3V MAX 180V~264V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	15.480
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	22.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.981	4.981	4.980
2.	12.600	11.400	12.062	12.062	12.062
3.	12.600	11.400	12.018	12.018	12.018
4.	-13.200	-10.920	-11.950	-11.950	-11.950
5.	3.465	3.135	3.242	3.242	3.241
6.	5.250	4.750	4.797	4.796	4.796

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.027	0.027	0.027
2.	0.120	*	0.022	0.023	0.025
3.	0.120	*	0.020	0.020	0.023
4.	0.120	*	0.020	0.020	0.020
5.	0.050	*	0.031	0.030	0.030
6.	0.050	*	0.010	0.009	0.010

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000

6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.007	0.006	0.006
3.	*	0.007	0.006	0.007
4.	*	0.007	0.007	0.007
5.	*	0.005	0.005	0.005
6.	*	0.001	0.001	0.001

SEQ.7: Line Regulation Test (1.+12V MAX 90V~132V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	5.640
2.	0000	0000	0000	0.010	16.000
3.	0000	0000	0000	0.010	16.000
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	8.540
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	5.042	5.041	5.041
2.	12.600	11.400	12.068	12.068	12.068
3.	12.600	11.400	12.012	12.018	12.018

4.	-13.200	-10.920	-11.900	-11.900	-11.900
5.	3.465	3.135	3.268	3.269	3.269
6.	5.250	4.750	4.860	4.860	4.861

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.026	0.026	0.026
2.	0.120	*	0.023	0.026	0.026
3.	0.120	*	0.022	0.022	0.023
4.	0.120	*	0.018	0.018	0.018
5.	0.050	*	0.006	0.006	0.005
6.	0.050	*	0.009	0.010	0.009

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	-0.001
2.	*	*	0.000	0.000
3.	*	*	0.006	0.006
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006
3.	*	0.006	0.007	0.006
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.8: Line Regulation Test (1.+12V MAX 180V~264V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500

2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	5.640
2.	0000	0000	0000	0.010	16.000
3.	0000	0000	0000	0.010	16.000
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	8.540
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	5.040	5.039	5.038
2.	12.600	11.400	12.068	12.068	12.068
3.	12.600	11.400	12.018	12.018	12.018
4.	-13.200	-10.920	-11.900	-11.900	-11.900
5.	3.465	3.135	3.269	3.268	3.268
6.	5.250	4.750	4.861	4.861	4.861

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.026	0.026	0.026
2.	0.120	*	0.025	0.025	0.024
3.	0.120	*	0.023	0.021	0.022
4.	0.120	*	0.018	0.018	0.018
5.	0.050	*	0.005	0.005	0.005
6.	0.050	*	0.009	0.008	0.009

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	-0.001
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.003	0.003
2.	*	0.006	0.006	0.006
3.	*	0.007	0.006	0.006
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.9: Line Regulation Test (1.+3.3V MAX 90V~132V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	3.000	Meas. Time =	3.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	20	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	24.060
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	9.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.938	4.940	4.941
2.	12.600	11.400	12.068	12.068	12.068
3.	12.600	11.400	12.025	12.025	12.025
4.	-13.200	-10.920	-11.931	-11.931	-11.931
5.	3.465	3.135	3.263	3.263	3.263

6.	5.250	4.750	4.811	4.811	4.811

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.028	0.027	0.027
2.	0.120	*	0.024	0.022	0.021
3.	0.120	*	0.023	0.021	0.022
4.	0.120	*	0.018	0.018	0.018
5.	0.050	*	0.010	0.010	0.010
6.	0.050	*	0.011	0.009	0.010

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.001	0.002
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006
3.	*	0.007	0.007	0.007
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.10: Line Regulation Test (1.+3.3V MAX 180V~264V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000

4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	24.060
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	9.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.941	4.941	4.941
2.	12.600	11.400	12.068	12.068	12.068
3.	12.600	11.400	12.025	12.025	12.025
4.	-13.200	-10.920	-11.931	-11.937	-11.931
5.	3.465	3.135	3.263	3.263	3.263
6.	5.250	4.750	4.811	4.811	4.811

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.027	0.026	0.026
2.	0.120	*	0.023	0.023	0.023
3.	0.120	*	0.021	0.021	0.021
4.	0.120	*	0.019	0.018	0.018
5.	0.050	*	0.009	0.009	0.009
6.	0.050	*	0.010	0.010	0.010

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.006	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004

2.	*	0.006	0.006	0.006
3.	*	0.007	0.007	0.007
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.11: Hold On Adjust (2. DAN) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Von	BITS
1.	+5V	I	50	10	UUT	300	3.500	0000
2.	+12V1	I	50	100	UUT	300	8.000	0000
3.	+12V2	I	50	100	UUT	300	8.000	0000
4.	-12V	I	50	100	UUT	300	-8.000	0000
5.	+3.3V	I	50	10	UUT	300	2.000	0000
6.	SB+5V	I	50	10	UUT	300	3.500	0000

Ld	I/R	Slew Rate	Vdc Max	Vdc Min	Vdc Read
1.	1.000	0.010	5.250	4.750	5.102
2.	1.000	0.010	12.600	11.400	12.150
3.	1.000	0.010	12.600	11.400	12.143
4.	0.100	0.010	-13.200	-10.920	-11.918
5.	1.000	0.010	3.465	3.135	3.300
6.	0.100	0.010	5.250	4.750	5.179

SEQ.12: Line Regulation Test (2.+5V MAX 90V~132V (DAN)) ---------- PASS

Vin Port-1 = 1 Vin Port-2 = 1 Vin Port-3 = 1
Vin-1 = 90.000 Vin-2 = 115.000 Vin-3 = 132.000
Fac = 60.0 Fac-2 = 60.0 Fac-3 = 60.0
Delay Time = 2.000 Meas. Time = 2.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000

3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	25.000
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	7.570
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.936	4.938	4.938
2.	12.600	11.400	12.068	12.068	12.068
3.	12.600	11.400	12.025	12.025	12.025
4.	-13.200	-10.920	-11.931	-11.931	-11.931
5.	3.465	3.135	3.265	3.265	3.265
6.	5.250	4.750	4.812	4.812	4.812

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.029	0.028	0.028
2.	0.120	*	0.021	0.023	0.023
3.	0.120	*	0.021	0.021	0.020
4.	0.120	*	0.018	0.020	0.018
5.	0.050	*	0.009	0.009	0.009
6.	0.050	*	0.011	0.010	0.011

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.001	0.001
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read

1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006
3.	*	0.007	0.007	0.007
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.13: Line Regulation Test (2.+5V MAX 180V~264V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	25.000
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	7.570
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.938	4.938	4.938
2.	12.600	11.400	12.068	12.068	12.068
3.	12.600	11.400	12.025	12.025	12.025
4.	-13.200	-10.920	-11.931	-11.931	-11.931
5.	3.465	3.135	3.265	3.265	3.265
6.	5.250	4.750	4.812	4.812	4.813

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.027	0.025	0.027
2.	0.120	*	0.021	0.022	0.023
3.	0.120	*	0.023	0.021	0.023
4.	0.120	*	0.018	0.017	0.019
5.	0.050	*	0.009	0.008	0.008
6.	0.050	*	0.012	0.011	0.010

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006
3.	*	0.007	0.007	0.007
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.14: Line Regulation Test (2.+3.3V MAX 90V~132V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000

5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	15.480
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	22.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.983	4.983	4.983
2.	12.600	11.400	12.068	12.068	12.068
3.	12.600	11.400	12.018	12.018	12.025
4.	-13.200	-10.920	-11.943	-11.950	-11.950
5.	3.465	3.135	3.241	3.241	3.241
6.	5.250	4.750	4.796	4.796	4.796

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.027	0.027	0.027
2.	0.120	*	0.025	0.023	0.023
3.	0.120	*	0.025	0.020	0.021
4.	0.120	*	0.019	0.019	0.020
5.	0.050	*	0.032	0.031	0.030
6.	0.050	*	0.009	0.011	0.011

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.000	0.000
3.	*	*	0.000	0.006
4.	*	*	0.006	0.006
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006

3.	*	0.007	0.007	0.007
4.	*	0.007	0.007	0.007
5.	*	0.005	0.005	0.005
6.	*	0.001	0.001	0.001

SEQ.15: Line Regulation Test (2.+3.3V MAX 180V~264V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	15.480
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	22.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.982	4.982	4.981
2.	12.600	11.400	12.062	12.062	12.068
3.	12.600	11.400	12.018	12.025	12.018
4.	-13.200	-10.920	-11.950	-11.950	-11.950
5.	3.465	3.135	3.241	3.241	3.241
6.	5.250	4.750	4.796	4.796	4.796

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD

1.	0.050	*	0.027	0.026	0.026
2.	0.120	*	0.023	0.023	0.023
3.	0.120	*	0.022	0.023	0.022
4.	0.120	*	0.020	0.018	0.019
5.	0.050	*	0.029	0.028	0.028
6.	0.050	*	0.009	0.009	0.009

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	-0.001
2.	*	*	0.000	0.006
3.	*	*	0.006	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006
3.	*	0.007	0.007	0.007
4.	*	0.007	0.007	0.007
5.	*	0.005	0.004	0.004
6.	*	0.001	0.001	0.001

SEQ.16: Line Regulation Test (2.+12V MAX 90V~132V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	5.640
2.	0000	0000	0000	0.010	16.000
3.	0000	0000	0000	0.010	16.000
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	8.540
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	5.043	5.043	5.042
2.	12.600	11.400	12.068	12.075	12.068
3.	12.600	11.400	12.018	12.018	12.018
4.	-13.200	-10.920	-11.900	-11.900	-11.900
5.	3.465	3.135	3.268	3.268	3.268
6.	5.250	4.750	4.860	4.860	4.860

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.026	0.026	0.026
2.	0.120	*	0.027	0.025	0.024
3.	0.120	*	0.023	0.021	0.021
4.	0.120	*	0.019	0.018	0.018
5.	0.050	*	0.006	0.005	0.005
6.	0.050	*	0.009	0.009	0.009

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	-0.001
2.	*	*	0.006	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006
3.	*	0.007	0.006	0.007
4.	*	0.007	0.007	0.007

5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.17: Line Regulation Test (2.+12V MAX 180V~264V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	5.640
2.	0000	0000	0000	0.010	16.000
3.	0000	0000	0000	0.010	16.000
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	8.540
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	5.041	5.040	5.039
2.	12.600	11.400	12.068	12.068	12.068
3.	12.600	11.400	12.018	12.018	12.018
4.	-13.200	-10.920	-11.900	-11.900	-11.900
5.	3.465	3.135	3.268	3.268	3.268
6.	5.250	4.750	4.861	4.861	4.861

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.026	0.026	0.026
2.	0.120	*	0.025	0.025	0.023

3.	0.120	*	0.022	0.021	0.021
4.	0.120	*	0.018	0.018	0.018
5.	0.050	*	0.005	0.005	0.006
6.	0.050	*	0.008	0.009	0.009

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	-0.001	-0.001
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.003
2.	*	0.006	0.006	0.006
3.	*	0.007	0.007	0.007
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.18: Line Regulation Test (2.+3.3V MAX 90V~132V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R

1.	0000	0000	0000	0.010	24.060
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	9.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.938	4.939	4.940
2.	12.600	11.400	12.068	12.068	12.068
3.	12.600	11.400	12.025	12.025	12.025
4.	-13.200	-10.920	-11.931	-11.931	-11.931
5.	3.465	3.135	3.262	3.262	3.263
6.	5.250	4.750	4.811	4.811	4.810

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.028	0.026	0.027
2.	0.120	*	0.022	0.021	0.023
3.	0.120	*	0.022	0.019	0.023
4.	0.120	*	0.018	0.018	0.018
5.	0.050	*	0.010	0.010	0.010
6.	0.050	*	0.010	0.012	0.011

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.001	0.001
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006
3.	*	0.007	0.007	0.007
4.	*	0.008	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.19: Line Regulation Test (2.+3.3V MAX 180V~264V (DAN)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	24.060
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	9.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.940	4.940	4.940
2.	12.600	11.400	12.068	12.068	12.068
3.	12.600	11.400	12.025	12.025	12.025
4.	-13.200	-10.920	-11.937	-11.931	-11.931
5.	3.465	3.135	3.262	3.262	3.262
6.	5.250	4.750	4.811	4.810	4.811

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.026	0.026	0.024
2.	0.120	*	0.023	0.022	0.025
3.	0.120	*	0.022	0.020	0.023
4.	0.120	*	0.019	0.018	0.018

5.	0.050	*	0.009	0.010	0.009
6.	0.050	*	0.009	0.010	0.010

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	-0.006	-0.006
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.004	0.004	0.004
2.	*	0.006	0.006	0.006
3.	*	0.007	0.007	0.007
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.20: Hold On Adjust (3. SHUANG) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Von	BITS
1.	+5V	I	50	10	UUT	300	3.500	0000
2.	+12V1	I	50	100	UUT	300	8.000	0000
3.	+12V2	I	50	100	UUT	300	8.000	0000
4.	-12V	I	50	100	UUT	300	-8.000	0000
5.	+3.3V	I	50	10	UUT	300	2.000	0000
6.	SB+5V	I	50	10	UUT	300	3.500	0000

Ld	I/R	Slew Rate	Vdc Max	Vdc Min	Vdc Read
1.	2.000	0.010	5.250	4.750	5.090
2.	1.500	0.010	12.600	11.400	12.137
3.	1.000	0.010	12.600	11.400	12.131
4.	0.200	0.010	-13.200	-10.920	-11.975
5.	2.000	0.010	3.465	3.135	3.298

6.	0.200	0.010	5.250	4.750	5.195

SEQ.21: Line Regulation Test (3.+5V MAX 90V~132V (SHUANG))) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	25.000
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	7.570
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.963	4.963	4.963
2.	12.600	11.400	12.031	12.031	12.025
3.	12.600	11.400	11.981	11.981	11.981
4.	-13.200	-10.920	-12.000	-12.000	-12.000
5.	3.465	3.135	3.268	3.268	3.268
6.	5.250	4.750	4.931	4.931	4.931

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.011	0.011	0.011
2.	0.120	*	0.012	0.013	0.012
3.	0.120	*	0.013	0.013	0.013

4.	0.120	*	0.013	0.013	0.015
5.	0.050	*	0.003	0.003	0.003
6.	0.050	*	0.005	0.005	0.005

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.000	-0.006
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.001	0.001	0.001
2.	*	0.006	0.006	0.006
3.	*	0.006	0.006	0.006
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.22: Line Regulation Test (3.+5V MAX 180V~264V (SHUANG)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	25.000

2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	7.570
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.962	4.962	4.961
2.	12.600	11.400	12.031	12.025	12.025
3.	12.600	11.400	11.981	11.981	11.981
4.	-13.200	-10.920	-12.006	-12.000	-12.000
5.	3.465	3.135	3.267	3.267	3.267
6.	5.250	4.750	4.931	4.931	4.931

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.011	0.011	0.011
2.	0.120	*	0.012	0.012	0.011
3.	0.120	*	0.013	0.013	0.015
4.	0.120	*	0.013	0.015	0.013
5.	0.050	*	0.003	0.004	0.004
6.	0.050	*	0.005	0.005	0.005

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	-0.006	-0.006
3.	*	*	0.000	0.000
4.	*	*	-0.006	-0.006
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.001	0.001	0.001
2.	*	0.006	0.006	0.006
3.	*	0.006	0.006	0.006
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.23: Line Regulation Test (3.+3.3V MAX 90V~132V (SHUANG)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	15.480
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	22.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.996	4.996	4.995
2.	12.600	11.400	12.025	12.025	12.018
3.	12.600	11.400	11.975	11.975	11.975
4.	-13.200	-10.920	-12.012	-12.012	-12.018
5.	3.465	3.135	3.239	3.238	3.238
6.	5.250	4.750	4.915	4.915	4.915

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.012	0.012	0.012
2.	0.120	*	0.011	0.011	0.013
3.	0.120	*	0.014	0.013	0.015
4.	0.120	*	0.014	0.015	0.015
5.	0.050	*	0.007	0.007	0.007

6.	0.050	*	0.006	0.006	0.006

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	-0.001
2.	*	*	0.000	-0.006
3.	*	*	0.000	0.000
4.	*	*	0.000	0.006
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.001	0.001	0.001
2.	*	0.006	0.006	0.006
3.	*	0.006	0.006	0.006
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.24: Line Regulation Test (3.+3.3V MAX 180V~264V (SHUANG)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	15.480
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760

4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	22.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.995	4.995	4.994
2.	12.600	11.400	12.018	12.025	12.025
3.	12.600	11.400	11.975	11.975	11.975
4.	-13.200	-10.920	-12.012	-12.018	-12.018
5.	3.465	3.135	3.238	3.238	3.238
6.	5.250	4.750	4.915	4.915	4.915

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.012	0.012	0.012
2.	0.120	*	0.012	0.013	0.012
3.	0.120	*	0.013	0.014	0.013
4.	0.120	*	0.015	0.013	0.016
5.	0.050	*	0.007	0.007	0.007
6.	0.050	*	0.005	0.006	0.006

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.006	0.006
3.	*	*	0.000	0.000
4.	*	*	0.006	0.006
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.001	0.001	0.001
2.	*	0.006	0.006	0.006
3.	*	0.006	0.006	0.006
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.25: Line Regulation Test (3.+12V MAX 90V~132V (SHUANG)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	90.000	Vin-2 =	115.000	Vin-3 =	132.000
Fac =	60.0	Fac-2 =	60.0	Fac-3 =	60.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	5.640
2.	0000	0000	0000	0.010	16.000
3.	0000	0000	0000	0.010	16.000
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	8.540
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	5.045	5.044	5.043
2.	12.600	11.400	12.018	12.018	12.018
3.	12.600	11.400	11.962	11.962	11.962
4.	-13.200	-10.920	-11.975	-11.975	-11.975
5.	3.465	3.135	3.270	3.270	3.270
6.	5.250	4.750	4.963	4.963	4.963

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.012	0.012	0.012
2.	0.120	*	0.011	0.013	0.012
3.	0.120	*	0.013	0.014	0.014
4.	0.120	*	0.014	0.015	0.013
5.	0.050	*	0.003	0.003	0.003
6.	0.050	*	0.004	0.004	0.004

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	-0.001
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.001	0.001	0.001
2.	*	0.006	0.006	0.006
3.	*	0.006	0.006	0.006
4.	*	0.007	0.008	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.26: Line Regulation Test (3.+12V MAX 180V~264V (SHUANG)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	5.640
2.	0000	0000	0000	0.010	16.000
3.	0000	0000	0000	0.010	16.000
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	8.540

6. 0000 0000 0000 0.010 2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	5.043	5.043	5.042
2.	12.600	11.400	12.018	12.018	12.018
3.	12.600	11.400	11.962	11.962	11.962
4.	-13.200	-10.920	-11.975	-11.975	-11.975
5.	3.465	3.135	3.270	3.270	3.270
6.	5.250	4.750	4.963	4.963	4.963

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.012	0.012	0.012
2.	0.120	*	0.012	0.013	0.012
3.	0.120	*	0.013	0.013	0.013
4.	0.120	*	0.013	0.015	0.014
5.	0.050	*	0.002	0.003	0.003
6.	0.050	*	0.004	0.004	0.004

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.001	0.001	0.001
2.	*	0.006	0.006	0.006
3.	*	0.006	0.006	0.006
4.	*	0.007	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.27: Line Regulation Test (3.+3.3V MAX 90V~132V (SHUANG)) ---------- PASS

Vin Port-1 = 1 Vin Port-2 = 1 Vin Port-3 = 1

Vin-1	= 90.000	Vin-2	= 115.000	Vin-3	= 132.000
Fac	= 60.0	Fac-2	= 60.0	Fac-3	= 60.0
Delay Time	= 2.000	Meas. Time	= 2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	24.060
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	9.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.961	4.962	4.962
2.	12.600	11.400	12.025	12.025	12.025
3.	12.600	11.400	11.981	11.975	11.975
4.	-13.200	-10.920	-12.000	-12.006	-12.000
5.	3.465	3.135	3.265	3.265	3.265
6.	5.250	4.750	4.928	4.928	4.928

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.011	0.011	0.011
2.	0.120	*	0.013	0.013	0.013
3.	0.120	*	0.013	0.013	0.013
4.	0.120	*	0.013	0.015	0.015
5.	0.050	*	0.004	0.004	0.004
6.	0.050	*	0.005	0.005	0.005

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000

2.	*	*	0.000	0.000
3.	*	*	-0.006	-0.006
4.	*	*	0.006	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.001	0.001	0.001
2.	*	0.006	0.006	0.006
3.	*	0.006	0.006	0.006
4.	*	0.007	0.008	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001

SEQ.28: Line Regulation Test (3.+3.3V MAX 180V~264V (SHUANG)) ---------- PASS

Vin Port-1 =	1	Vin Port-2 =	1	Vin Port-3 =	1
Vin-1 =	180.000	Vin-2 =	230.000	Vin-3 =	264.000
Fac =	50.0	Fac-2 =	50.0	Fac-3 =	50.0
Delay Time =	2.000	Meas. Time =	2.000		

	Load Name	MODE	Ifs	Vfs	Meas.	Vdc Filter	Noise Filter	Von
1.	+5V	I	50	10	UUT	300	1M	3.500
2.	+12V1	I	50	100	UUT	300	1M	8.000
3.	+12V2	I	50	100	UUT	300	1M	8.000
4.	-12V	I	50	100	UUT	300	1M	-8.000
5.	+3.3V	I	50	10	UUT	300	1M	2.000
6.	SB+5V	I	50	10	UUT	300	1M	3.500

Ld	BITS-1	BITS-2	BITS-3	SLEW Rate	I/R
1.	0000	0000	0000	0.010	24.060
2.	0000	0000	0000	0.010	13.760
3.	0000	0000	0000	0.010	13.760
4.	0000	0000	0000	0.010	0.800
5.	0000	0000	0000	0.010	9.000
6.	0000	0000	0000	0.010	2.000

Ld	Vdc Max	Vdc Min	Vdc-1 RD	Vdc-2 RD	Vdc-3 RD
1.	5.250	4.750	4.961	4.962	4.961
2.	12.600	11.400	12.025	12.025	12.025
3.	12.600	11.400	11.975	11.975	11.975
4.	-13.200	-10.920	-12.000	-12.000	-12.000
5.	3.465	3.135	3.264	3.265	3.265
6.	5.250	4.750	4.927	4.927	4.927

Ld	Vpp Max	Vpp Min	Vpp-1 RD	Vpp-2 RD	Vpp-3 RD
1.	0.050	*	0.011	0.011	0.011
2.	0.120	*	0.013	0.011	0.011
3.	0.120	*	0.013	0.013	0.013
4.	0.120	*	0.013	0.015	0.016
5.	0.050	*	0.004	0.004	0.004
6.	0.050	*	0.005	0.005	0.006

Ld	dV(+) Max	dV(-) Max	dV-21 RD	dV-31 RD
1.	*	*	0.000	0.000
2.	*	*	0.000	0.000
3.	*	*	0.000	0.000
4.	*	*	0.000	0.000
5.	*	*	0.000	0.000
6.	*	*	0.000	0.000

Ld	Vn Max	Vn-1 Read	Vn-2 Read	Vn-3 Read
1.	*	0.001	0.001	0.001
2.	*	0.006	0.006	0.006
3.	*	0.006	0.006	0.007
4.	*	0.008	0.007	0.007
5.	*	0.000	0.000	0.000
6.	*	0.001	0.001	0.001
